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PR-2023-0323-106698



Republic of the Philippines  
**VISAYAS STATE UNIVERSITY**  
 Visca, Baybay City, Leyte

## PURCHASE REQUEST

PPMP No. : 200-1-2088-2023-2-0-1

PR No.: STF-2023-03-00487

Date: 03-23-2023

Dept./Office: DPhys

Category: Laboratory  
Equipment

Section/End-User: Perlle A. Gucela

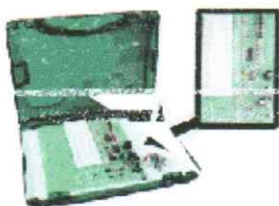
Project Title/Code: Advanced Physics Laboratory-  
2024B

Funding Source: Special Trust Fund

Item #	Item Description	Unit	Qty	Unit Cost	PAR/ICS	Total Cost
1	Circuit Training Board with Power Supply	unit	5	15,000.00		75,000.00

### Specification:

- **8 Data Switches** - The data switch circuits provides eight DATA OUTPUT pins D1 to D8 whose output level is adjusted using slide switches.
- **8 Data Status Monitor** - Has eight buffered inputs with LED indicators that show the current state of the inputs.
- **555-Based Clock Generator** - The circuit is based on a 555 IC configured as an astable multivibrator. The signal is accessible through the OUT pin. An overlapping LOW (10 Hz to 500 Hz) and HIGH (20 Hz to 1 kHz) range switch is provided for frequency control as well as VR1 for fine tuning control.
- **Pulse Generator** - A monostable signal generator based on the 74HC221 IC. These are two outputs, pressing a switch generates a pulse across the corresponding output pin.
- **Logic Probe** - The probe displays the status of the signal fed across its input pins using three LED status indicators, one each for HIGH, LOW and PULSE.
- **5-Volt Power Supply** - This supply is the same power source used for the rest of the modules such that the user need not worry about compatibility problems when using the built-in modules in the trainer.



<b>TOTAL</b>						<b>75,000.00</b>
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Purpose: for Advanced Physics Laboratory

Checked by:		Funds Available:	
	<b>ELIZABETH S. QUEVEDO</b>		<b>ALICIA M. FLORES</b>
	TWG - Laboratory Equipment		HEAD, BUDGET OFFICE
Signature:	Requested by:	Noted by:	Approved by:
Printed Name:			
Designation:	<b>PERLIE A. GUCELA</b>	<b>REV RHIZZA L. AURE</b>	<b>EDGARDO E. TULIN</b>
	END USER	UNIT HEAD, PROJECT LEADER	PRESIDENT, VSU